



PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY
AUTONOMOUS
KANURU, VIJAYAWADA – 520 007

I M.TECH – II SEMESTER (PVP17) INTERNAL TIME TABLE

Attention: All I M.Tech Students

Second Descriptive midterm examinations for I M.Tech II Semester (PVP17) courses will be conducted as per the following schedule. All the students are advised to note the time table and report to the examination hall 10 minutes prior to the commencement of examination.

T I M E : 10.00 AM TO 11-30 AM

| BRANCH | DATE AND DAY | | | | | |
|--|--|--|--|--|---|--|
| | 30-07-2018 MONDAY | 31-07-2018 TUESDAY | 01-08-2018 WEDNESDAY | 02-08-2018 THURSDAY | 03-08-2018 FRIDAY | 04-08-2018 SATURDAY |
| MACHINE DESIGN (MECHANICAL) | MECHANISM DESIGN AND SYNTHESIS (17MEMD2T1) | ADVANCED OPTIMIZATION TECHNIQUES (17MEMD2T2) | FINITE ELEMENT METHODS IN ENGINEERING (17MEMD2T3) | ADVANCED ROBOTICS (17MEMD2T4) | FRACTURE MECHANICS (17MEMD2T5A) | CONCURRENT ENGINEERING (17MEMD2T6C) |
| POWER SYSTEM & CONTROL (EEE) | POWER SYSTEM STABILITY AND CONTROL (17EEPC2T1) | ECONOMIC OPERATION OF POWER SYSTEMS (17EEPC2T2) | REAL TIME CONTROL OF POWER SYSTEMS (17EEPC2T3) | DISTRIBUTION SYSTEM PLANNING & AUTOMATION (17EEPC2T4) | DIGITAL CONTROL SYSTEMS (17EEPC2T5B) | SMART GRID TECHNOLOGIES & APPLICATIONS (17EEPC2T6A) |
| MICROWAVE & COMMUNICATION ENGINEERING (ECE) | COMPUTATIONAL ELECTRO-MAGNETICS (17ECMC2T1) | SIGNAL PROCESSING FOR COMMUNICATIONS (17ECMC2T2) | MICROWAVE NETWORKS (17ECMC2T3) | OPTICAL NETWORKS (17ECMC2T4) | DSP PROCESSORS (17ECMC2T5C) | PHASED ARRAYS (17ECMC2T6C) |
| COMPUTER SCIENCE & ENGINEERING (CSE) | MOBILE APPLICATION DEVELOPMENT (17CSCS2T1) | BIG DATA ANALYTICS (17CSCS2T2) | CLOUD COMPUTING (17CSCS2T3) | INDEXING AND SEARCHING IN LARGE DATASETS (17CSCS2T4) | DEEP LEARNING (17CSCS2T5C) | CRYPTOGRAPHY & NETWORK SECURITY (17CSCS2T6A) |

NOTE: Any Omissions / Clashes / Discrepancies (if any) may kindly be brought to the notice of the Controller of Examinations immediately.

CONTROLLER OF EXAMINATIONS

DATE: 13-07-2018

Controller of Examinations

Prasad V. Potluri

CC to: Principal, Exam cell, M.Tech Programme HOD's
KANURU, VIJAYAWADA-520 007

PRINCIPAL
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