



PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY
AUTONOMOUS
 KANURU, VIJAYAWADA – 520 007

I M.TECH - II SEMESTER – (AUTONOMOUS BATCH - PVP 17 REGULATIONS) REGULAR EXAMINATIONS, AUGUST - 2018

TIME TABLE

TIME : 10.00 AM TO 01-00 PM

BRANCH	DATE AND DAY					
	13-08-2018 MONDAY	16-08-2018 THURSDAY	18-08-2018 SATURDAY	20-08-2018 MONDAY	23-08-2018 THURSDAY	25-08-2018 SATURDAY
MACHINE DESIGN (MECHANICAL)	MECHANISM DESIGN AND SYNTHESIS (17MEMD2T1)	ADVANCED OPTIMIZATION TECHNIQUES (17MEMD2T2)	FINITE ELEMENT METHODS IN ENGINEERING (17MEMD2T3)	ADVANCED ROBOTICS (17MEMD2T4)	FRACTURE MECHANICS (17MEMD2T5A)	CONCURRENT ENGINEERING (17MEMD2T6C)
POWER SYSTEM & CONTROL (EEE)	POWER SYSTEM STABILITY AND CONTROL (17EEPC2T1)	ECONOMIC OPERATION OF POWER SYSTEMS (17EEPC2T2)	REAL TIME CONTROL OF POWER SYSTEMS (17EEPC2T3)	DISTRIBUTION SYSTEM PLANNING & AUTOMATION (17EEPC2T4)	DIGITAL CONTROL SYSTEMS (17EEPC2T5B)	SMART GRID TECHNOLOGIES & APPLICATIONS (17EEPC2T6A)
MICROWAVE & COMMUNICATION ENGINEERING (ECE)	COMPUTATIONAL ELECTRO-MAGNETICS (17ECMC2T1)	SIGNAL PROCESSING FOR COMMUNICATIONS (17ECMC2T2)	MICROWAVE NETWORKS (17ECMC2T3)	OPTICAL NETWORKS (17ECMC2T4)	DSP PROCESSORS (17ECMC2T5C)	PHASED ARRAYS (17ECMC2T6C)
COMPUTER SCIENCE & ENGINEERING (CSE)	MOBILE APPLICATION DEVELOPMENT (17CSCS2T1)	BIG DATA ANALYTICS (17CSCS2T2)	CLOUD COMPUTING (17CSCS2T3)	INDEXING AND SEARCHING IN LARGE DATASETS (17CSCS2T4)	DEEP LEARNING (17CSCS2T5C)	CRYPTOGRAPHY & NETWORK SECURITY (17CSCS2T6A)

NOTE: Any Omissions / Clashes / Discrepancies (if any) may kindly be brought to the notice of the Controller of Examinations immediately.

CONTROLLER OF EXAMINATIONS

DATE: 13-07-2018

Prasad V. Potluri
 Controller of Examinations

Prasad V. Potluri

Siddhartha Institute of Technology
 C/O: Principal, Exam cell - M. Tech Programme HOD's
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