



PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY
AUTONOMOUS
KANURU, VIJAYAWADA – 520 007

II B.TECH – II SEMESTER–(AUTONOMOUS - PVP 12 REGULATIONS) SUPPLEMENTARY EXAMINATIONS, APRIL - 2017

TIME TABLE

TIME : 10.00 AM TO 01-00 PM

BRANCH	DATE AND DAY					
	04-04-2017 Tuesday	07-04-2017 Friday	11-04-2017 Tuesday	13-04-2017 Thursday	17-04-2017 Monday	19-04-2017 Wednesday
CIVIL ENGINEERING	CONCRETE TECHNOLOGY (CE4T1)	GEOTECHNICAL ENGINEERING-I (CE4T2)	MECHANICS OF SOLIDS-II (CE4T3)	HYDRAULICS & HYDRAULIC MACHINERY (CE4T4)	BUILDING PLANNING & DRAWING (CE4T5)	STRUCTURAL ANALYSIS-I (CE4T6)
ELECTRICAL AND ELECTRONICS ENGINEERING	INDUSTRIAL ORGANISATION AND ENGG. ECONOMICS (EE4T1)	ELECTRICAL MEASUREMENTS AND INSTRUMENTATION (EE4T2)	TRANSMISSION AND DISTRIBUTION (EE4T3)	ELECTRICAL MACHINES-II (EE4T4)	MATERIAL SCIENCE (EE4T5)	PULSE AND DIGITAL CIRCUITS (EE4T6)
MECHANICAL ENGINEERING	IC ENGINES AND GAS TURBINES (ME4T3)	APPLIED THERMODYNAMICS (ME4T2)	MECHANICS OF SOLIDS-II (ME4T1)	KINEMATICS OF MACHINERY (ME4T4)	PRODUCTION TECHNOLOGY (ME4T5)	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING (ME4T6)
ELECTRONICS & COMMUNICATIONS ENGINEERING	CONTROL SYSTEMS (EC4T1)	SWITCHING THEORY & LOGIC DESING (EC4T3)	ELECTROMAGNETIC FIELD THEORY (EC4T4)	ANALOG COMMUNICATIONS (EC4T5)	LINEAR IC APPLICATIONS (EC4T6)	PULSE AND DIGITAL CIRCUITS (EC4T2)
COMPUTER SCIENCE & ENGINEERING	THEORY OF COMPUTATION (CS4T1)	PRINCIPLES OF PROGRAMMING LANGUAGES (CS4T2)	OBJECT ORIENTED PROGRAMMING THROUGH JAVA (CS4T3)	FILE STRUCTURES (CS4T4)	MICRO PROCESSORS AND INTERFACING (CS4T5)	----

