



PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY
AUTONOMOUS
KANURU, VIJAYAWADA – 520 007

II B.TECH – I SEMESTER–(AUTONOMOUS BATCH - PVP 12 REGULATIONS) SUPPLEMENTARY EXAMINATIONS, APRIL - 2017

TIME TABLE

TIME: 02.00 PM TO 05-00 PM

| BRANCH | DATE AND DAY | | | | | |
|---|--|---|---|---|--|--|
| | 03-04-2017 Monday | 06-04-2017 Thursday | 10-04-2017 Monday | 12-04-2017 Wednesday | 15-04-2017 Saturday | 18-04-2017 Tuesday |
| CIVIL ENGINEERING | MATHEMATICAL METHODS (CE3T1) | BUILDING MATERIALS AND CONSTRUCTION (CE3T2) | MECHANICS OF SOLIDS – I (CE3T3) | ENGINEERING GEOLOGY (CE3T4) | SURVEYING (CE3T5) | FLUID MECHANICS (CE3T6) |
| ELECTRICAL AND ELECTRONICS ENGINEERING | ELECTRICAL MACHINES-I (EE3T1) | ELECTROMAGNETIC FIELDS (EE3T2) | ELECTRICAL POWER GENERATION (EE3T3) | SWITCHING THEORY AND LOGIC DESIGN (EE3T4) | ELECTRICAL CIRCUIT ANALYSIS-II (EE3T5) | NUMERICAL METHODS WITH C PROGRAMMING (EE3T6) |
| MECHANICAL ENGINEERING | MATHEMATICS-III (ME3T5) | BASIC THERMODYNAMICS (ME3T2) | FLUID MECHANICS & HYDRAULIC MACHINES (ME3T3) | METALLURGY & MATERIALS SCIENCE (ME3T4) | MECHANICS OF SOLIDS-I (ME3T1) | ENGINEERING ECONOMICS (ME3T6) |
| ELECTRONICS & COMMUNICATIONS ENGINEERING | ENGINEERING MATHEMATICS-III (EC3T1) | SIGNALS AND SYSTEMS (EC3T3) | PROBABILITY THEORY AND STOCHASTIC PROCESS (EC3T2) | ANALOG ELECTRONIC CIRCUITS (EC3T4) | ELECTRICAL TECHNOLOGY (EC3T5) | ELEMENTS OF MECHANICAL ENGINEERING (EC3T6) |
| COMPUTER SCIENCE & ENGINEERING | MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE (CS3T1) | COMPUTER ORGANIZATION (CS3T4) | DATA STRUCTURES (CS3T3) | OBJECT ORIENTED PROGRAMMING THROUGH C++ (CS3T2) | INFORMATION THEORY (CS3T5) | --- |

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