

PRASAD V POTLURI
SIDDHARTHA INSTITUTE OF TECHNOLOGY
KANURU – VIJAYAWADA

Dt : 23-03-2022

The following Students are benefited* by Revaluation in **II B.Tech I Semester (PVP20) Regular Examinations held in February-2022.**

Subject code	Subject Name	Reg. No.
20EC3301	ELECTRONIC DEVICES AND AMPLIFIER CIRCUITS	20501A0446
20CS3301	FUNDAMENTALS OF DIGITAL LOGIC DESIGN	20501A0570
20CS3301	FUNDAMENTALS OF DIGITAL LOGIC DESIGN	20501A0598
20IT3303	OBJECT ORIENTED PROGRAMMING THROUGH C++	20501A1231
20IT3303	OBJECT ORIENTED PROGRAMMING THROUGH C++	20501A1239
20CS3303	COMPUTER ORGANIZATION AND ARCHITECTURE	20501A0539



Controller of Examinations

Note: * Passed in the Examination or Improvement in Grade of the Student

PRASAD V POTLURI
SIDDHARTHA INSTITUTE OF TECHNOLOGY
KANURU – VIJAYAWADA

Dt : 23-03-2022

The following Students are benefited* by Recounting in II B.Tech I Semester (PVP20) Regular Examinations held in February-2022.

---NIL---



Controller of Examinations

Note: * Passed in the Examination or Improvement in Grade of the Student