



PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY
AUTONOMOUS
KANURU, VIJAYAWADA – 520 007

I B.TECH - II SEMESTER – (AUTONOMOUS BATCH - PVP 14 REGULATIONS) REGULAR/SUPPLEMENTARY EXAMINATIONS,
MAY-2017

TIME TABLE

TIME : 10.00 AM TO 01-00 PM

BRANCH	DATE AND DAY					
	02-05-2017 Tuesday	04-05-2017 Thursday	06-05-2017 Saturday	09-05-2017 Tuesday	11-05-2017 Thursday	13-05-2017 Saturday
CIVIL ENGINEERING	ENGINEERING MATHEMATICS – II (CE2T1)	PROFESSIONAL ETHICS (CE2T2)	ENGINEERING PHYSICS (CE2T3)	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING (CE2T5)	ENGINEERING MECHANICS (CE2T4)	C PROGRAMMING (CE2T6)
COMPUTER SCIENCE & ENGINEERING	ENGINEERING MATHEMATICS – II (CS2T1)	PROFESSIONAL ETHICS (CS2T2)	ENGINEERING PHYSICS (CS2T3)	DIGITAL LOGIC DESIGN (CS2T4)	BASIC ELECTRONICS ENGINEERING (CS2T5)	C PROGRAMMING (CS2T6)
ELECTRONICS & COMMUNICATIONS ENGINEERING	ENGINEERING MATHEMATICS – II (EC2T1)	PROFESSIONAL ETHICS (EC2T2)	ELEMENTS OF MECHANICAL ENGINEERING (EC2T3)	ENVIRONMENTAL STUDIES (EC2T4)	ELECTRONIC DEVICES AND CIRCUITS (EC2T5)	ENGINEERING DRAWING (EC2T6)
ELECTRICAL AND ELECTRONICS ENGINEERING	ENGINEERING MATHEMATICS – II (EE2T1)	PROFESSIONAL ETHICS (EE2T2)	ENGINEERING CHEMISTRY (EE2T3)	BASIC ELECTRONIC DEVICES AND CIRCUITS (EE2T4)	ELECTRICAL CIRCUIT ANALYSIS-I (EE2T5)	C PROGRAMMING (EE2T6)

