



**PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY**  
**AUTONOMOUS**  
 KANURU, VIJAYAWADA – 520 007

**I B.TECH-II SEMESTER-(AUTONOMOUS BATCH - PVP 12 REGULATIONS) SUPPLEMENTARY EXAMINATIONS, MAY-2017**

**TIME TABLE**

**TIME: 10.00 AM TO 01-00 PM**

BRANCH	DATE AND DAY					
	02-05-2017 Tuesday	04-05-2017 Thursday	06-05-2017 Saturday	09-05-2017 Tuesday	11-05-2017 Thursday	13-05-2017 Saturday
CIVIL ENGINEERING	ENGINEERING MATHEMATICS – II (CE2T2)	ENVIRONMENTAL STUDIES (CE2T6)	PROFESSIONAL ETHICS (CE2T1)	ENGINEERING PHYSICS (CE2T3)	ENGINEERING MECHANICS (CE2T5)	BASIC ELECTRICAL & ELECTRONICS ENGG. (CE2T4)
ELECTRICAL AND ELECTRONICS ENGINEERING	ENGINEERING MATHEMATICS – II (EE2T1)	ENVIRONMENTAL STUDIES (EE2T6)	ELECTRICAL CIRCUIT ANALYSIS-I (EE2T5)	ENGINEERING PHYSICS (EE2T2)	ELECTRONIC DEVICES AND CIRCUITS (EE2T4)	ENGINEERING CHEMISTRY (EE2T3)
MECHANICAL ENGINEERING	ENGINEERING MATHEMATICS – II (ME2T1)	BASIC MECHANICAL ENGG. (ME2T5)	PROFESSIONAL ETHICS (ME2T6)	ENGINEERING PHYSICS (ME2T2)	ENGINEERING MECHANICS-II (ME2T4)	ENGINEERING CHEMISTRY (ME2T3)
ELECTRONICS & COMMUNICATIONS ENGINEERING	ENGINEERING MATHEMATICS – II (EC2T1)	ENGLISH (EC2T2)	PROFESSIONAL ETHICS (EC2T5)	NETWORK THEORY (EC2T3)	ELECTRONIC DEVICES AND CIRCUITS (EC2T4)	-----
COMPUTER SCIENCE & ENGINEERING	ENGINEERING MATHEMATICS – II (CS2T1)	ENVIRONMENTAL STUDIES (CS2T5)	DIGITAL LOGIC DESIGN (CS2T4)	BASIC ELECTRICAL ENGG. (CS2T2)	ELECTRONIC DEVICES AND CIRCUITS (CS2T3)	-----

